1/6 Denneau et al. YO999-493 (JPS) (8728-334)



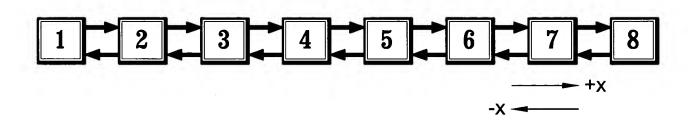
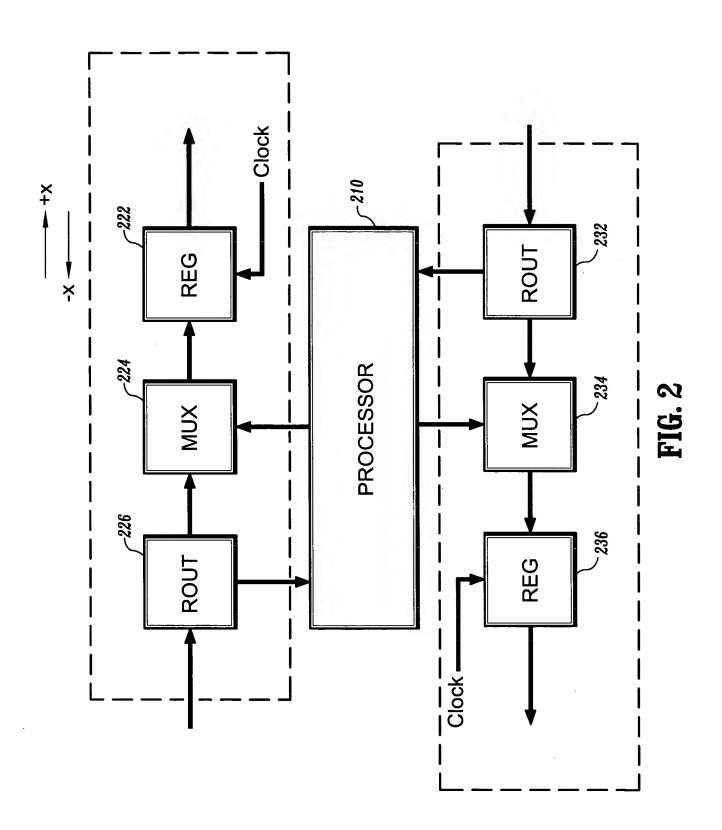
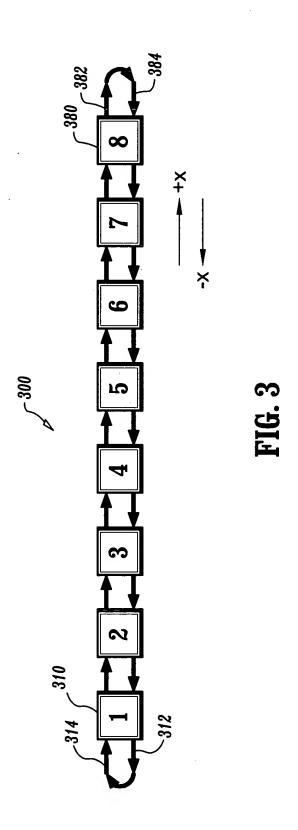


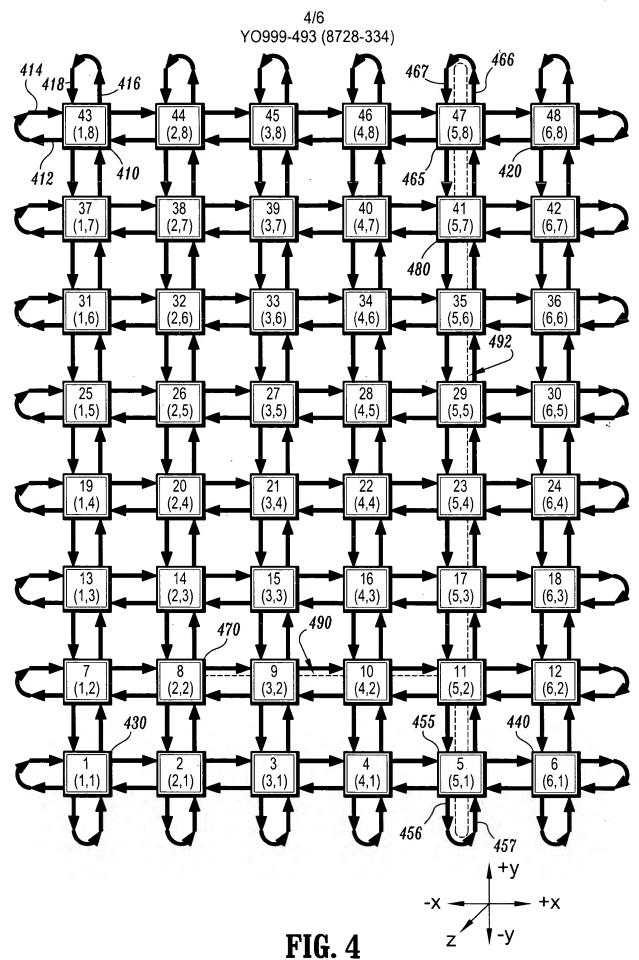
FIG. 1 (Prior Art)

2/6 YO999-493 (8728-334)



3/6 YO999-493 (8728-334)





5/6 YO999-493 (8728-334) Start FIG. 5 510 For each end processor, connect unused outputs to corresponding unused inputs 514 ls a result of directly Yes sending data packet < N/2 moves? 516 ls a result of directly Yes sending data packet > N/2 moves? *522* Random 520a 518a Add 0-bit or 1-bit to packet, Add 0-bit or 1-bit to packet, depending on if packet is to be depending on if packet is to be injected in the positive or the injected in the positive or the -520 518~ negative direction, respectively negative direction, respectively 520b 518b Place packet in a first or a Place packet in a first or a second queue, depending on if second queue, depending on if 0-bit or 1-bit is added to packet, 0-bit or 1-bit is added to packet, respectively respectively 520c 518c -Indirectly send packet Directly send packet so as to wrap around end processors End

